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PATENT
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Ohtani, et al. Art Unit: 1765
Serial No.: 08/690,747 Examiner: R. Kunemund
Filed : 8/1/96
Title : METHOD FOR MANUFACTURING SEMICONDUCTOR DEVICE

Assistant Commissioner for Patents
Washington, DC 20231

TRANSMITTAL OF ENGLISH TRANSLATION

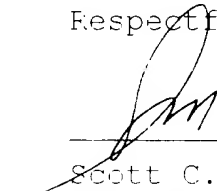
Pursuant to 37 CFR 1.55(a), enclosed herewith is an English translation of the filed Japanese specification no. 7-216608 for the above-identified patent application and the executed verification of translation.

Please charge any fees due for this paper or to satisfy any other requirements to Deposit Account No. 06-1050.

Respectfully submitted,

Date:

8/26/99

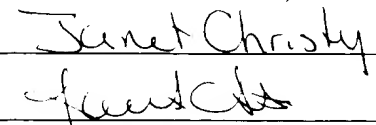

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